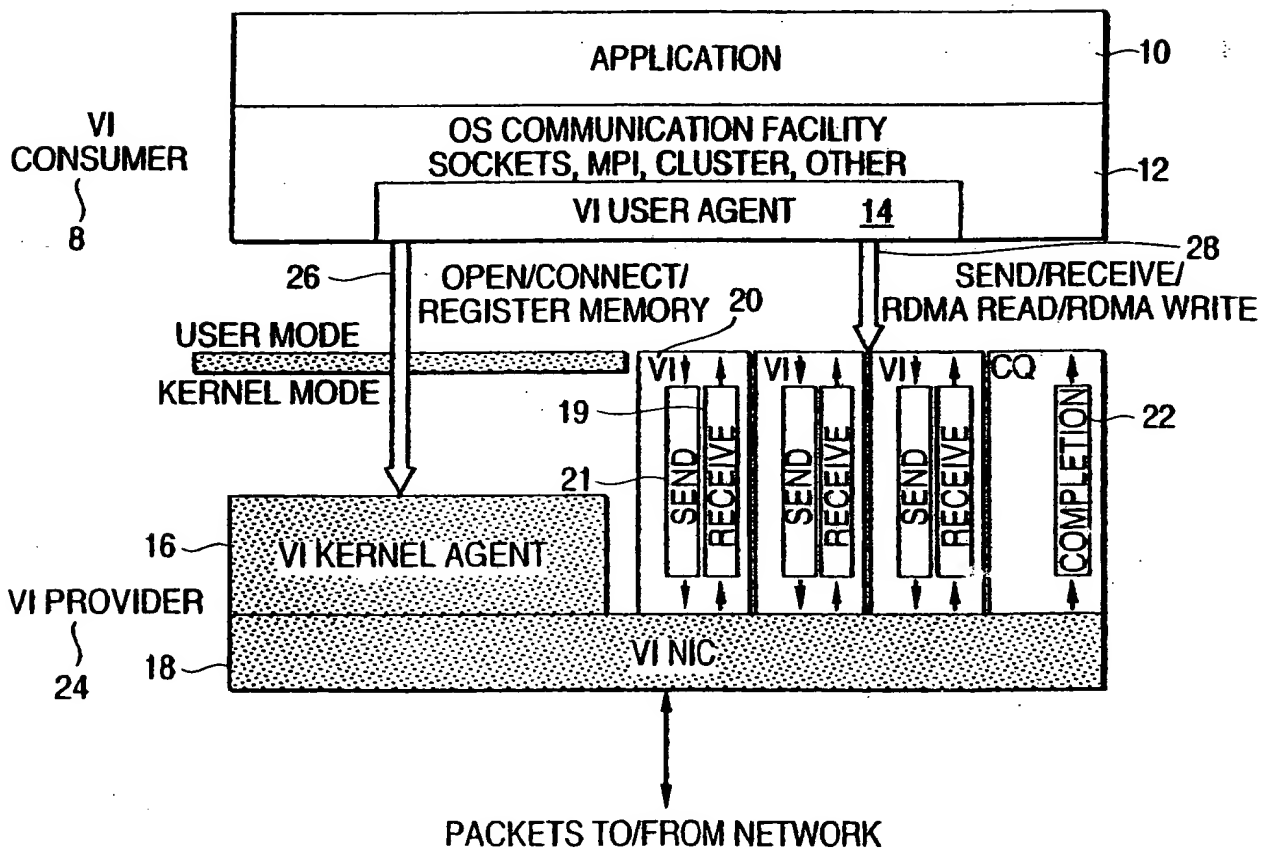


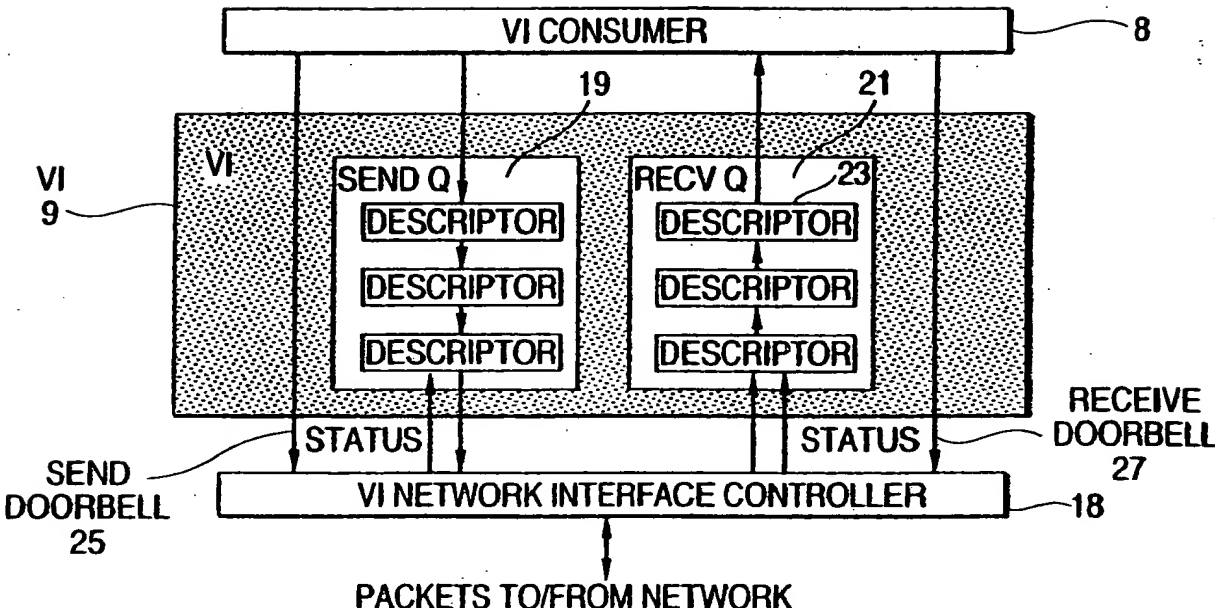
FIG. 1A

VI ARCHITECTURAL MODEL



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FIG. 1B



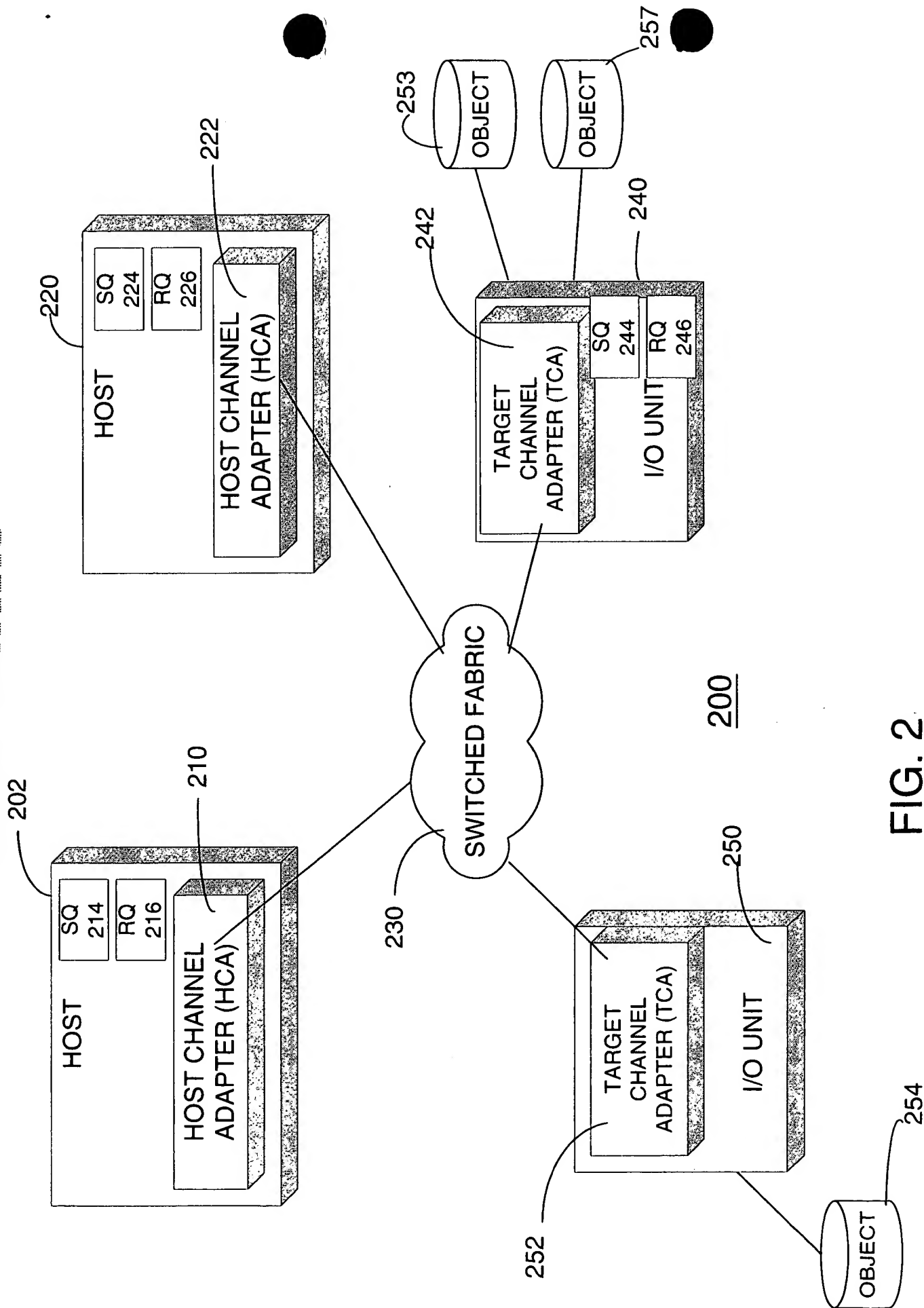


FIG. 2

$\frac{1}{\sqrt{2}} \begin{pmatrix} 1 & i \\ 0 & 1 \end{pmatrix}$ $\frac{1}{\sqrt{2}} \begin{pmatrix} 1 & -i \\ 0 & 1 \end{pmatrix}$ $\frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$ $\frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$ $\frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$

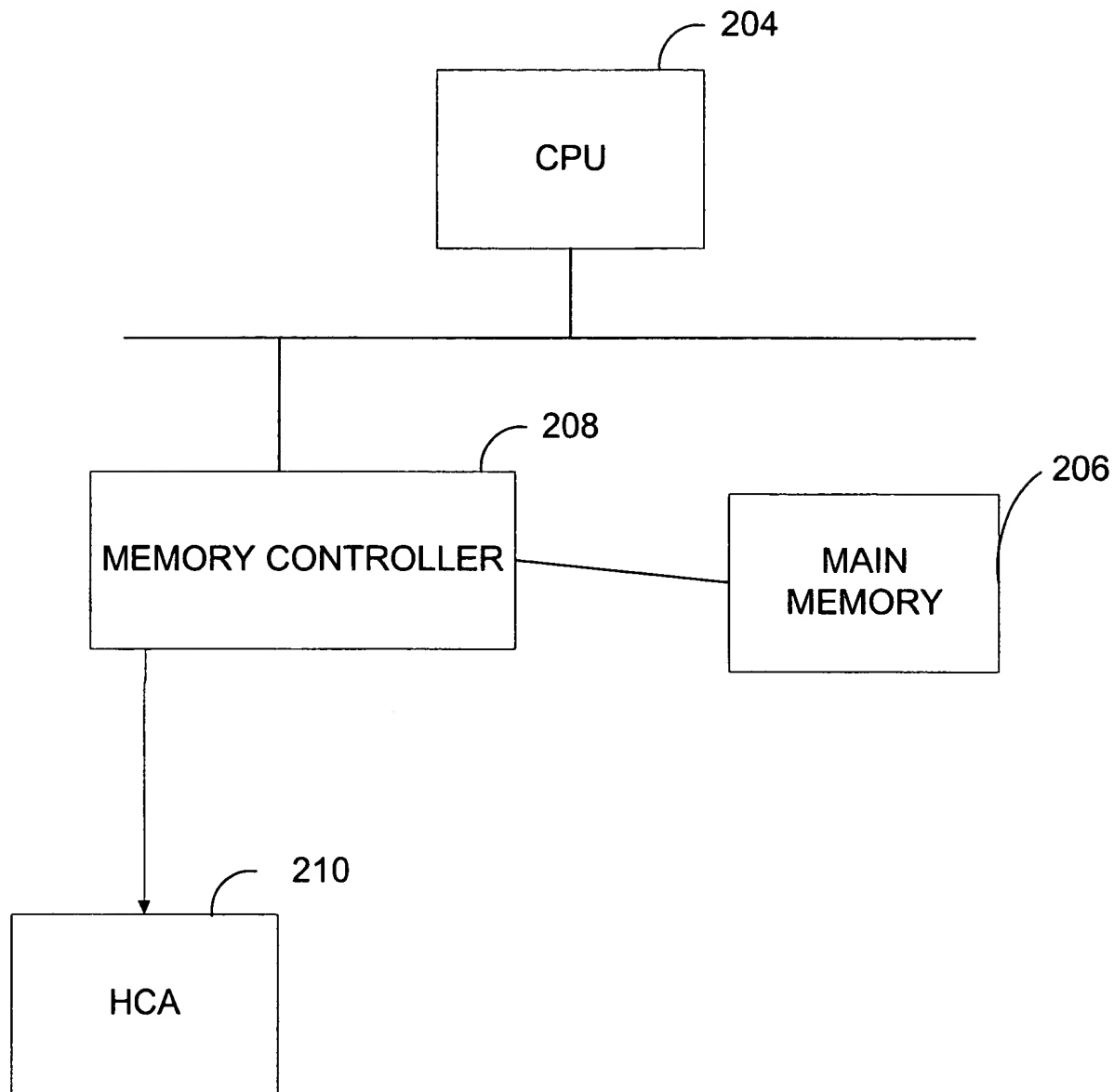
HOST 202

FIG. 3

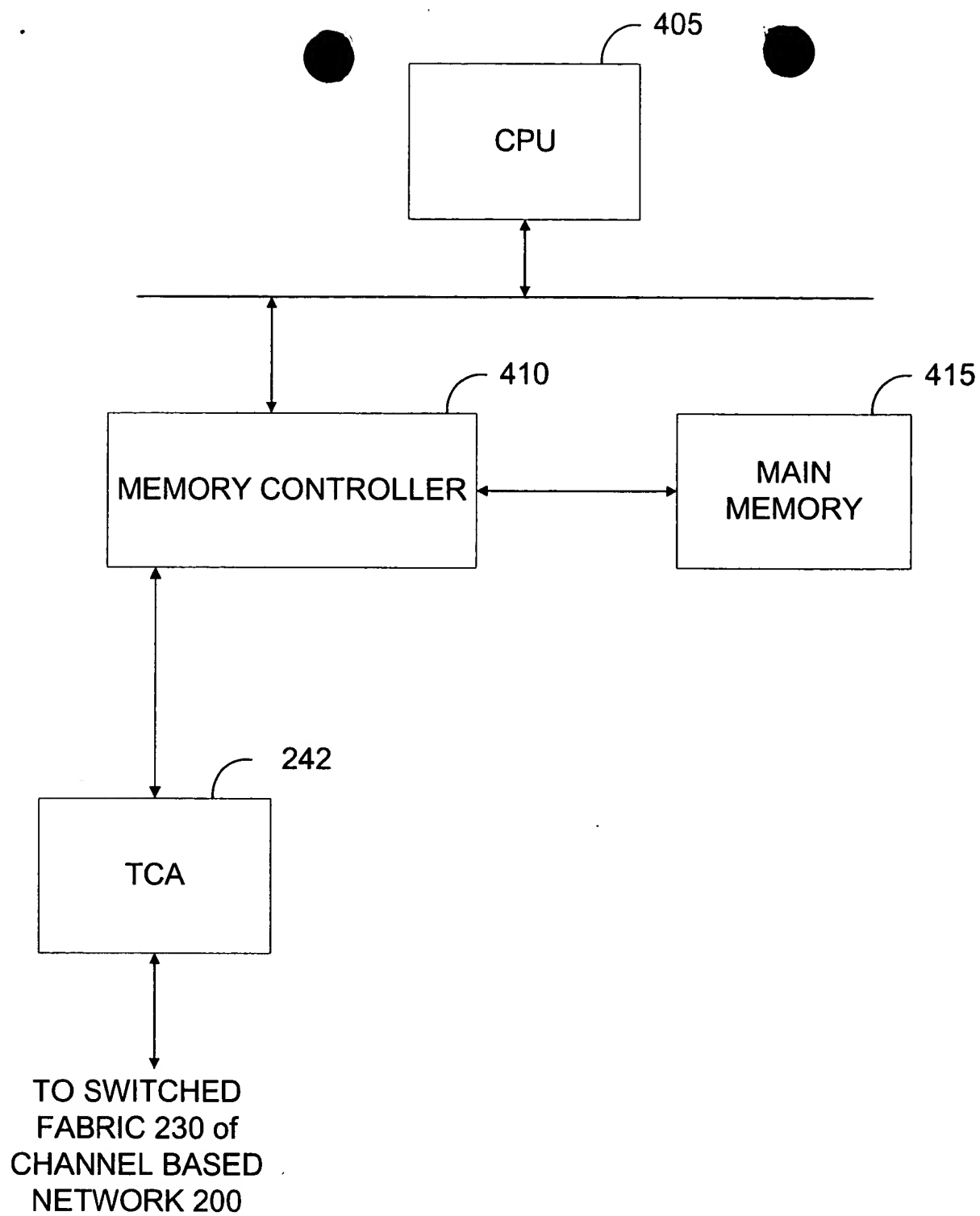
I/O UNIT 240

FIG. 4

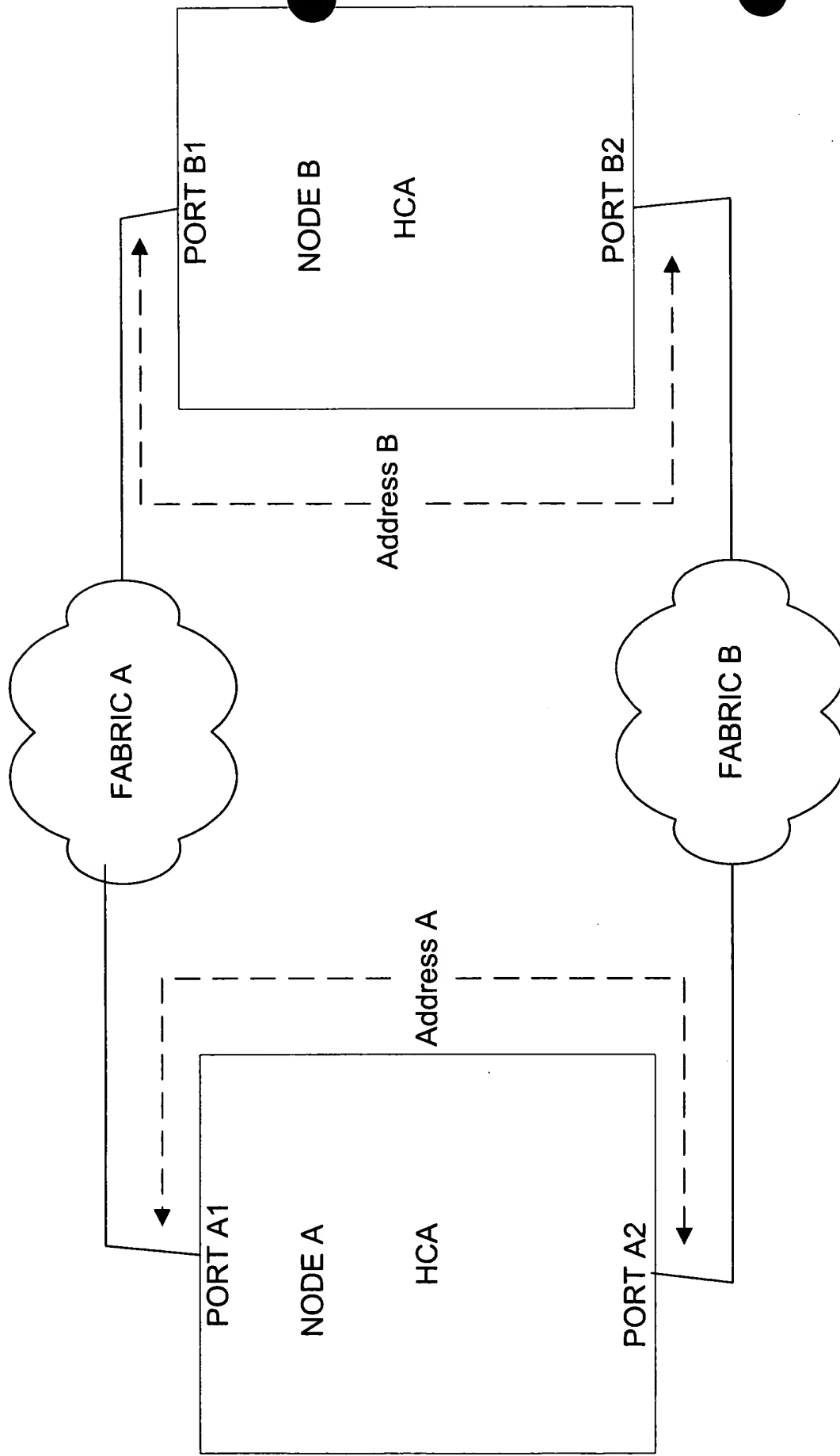


Fig. 5

DocId: 64302660

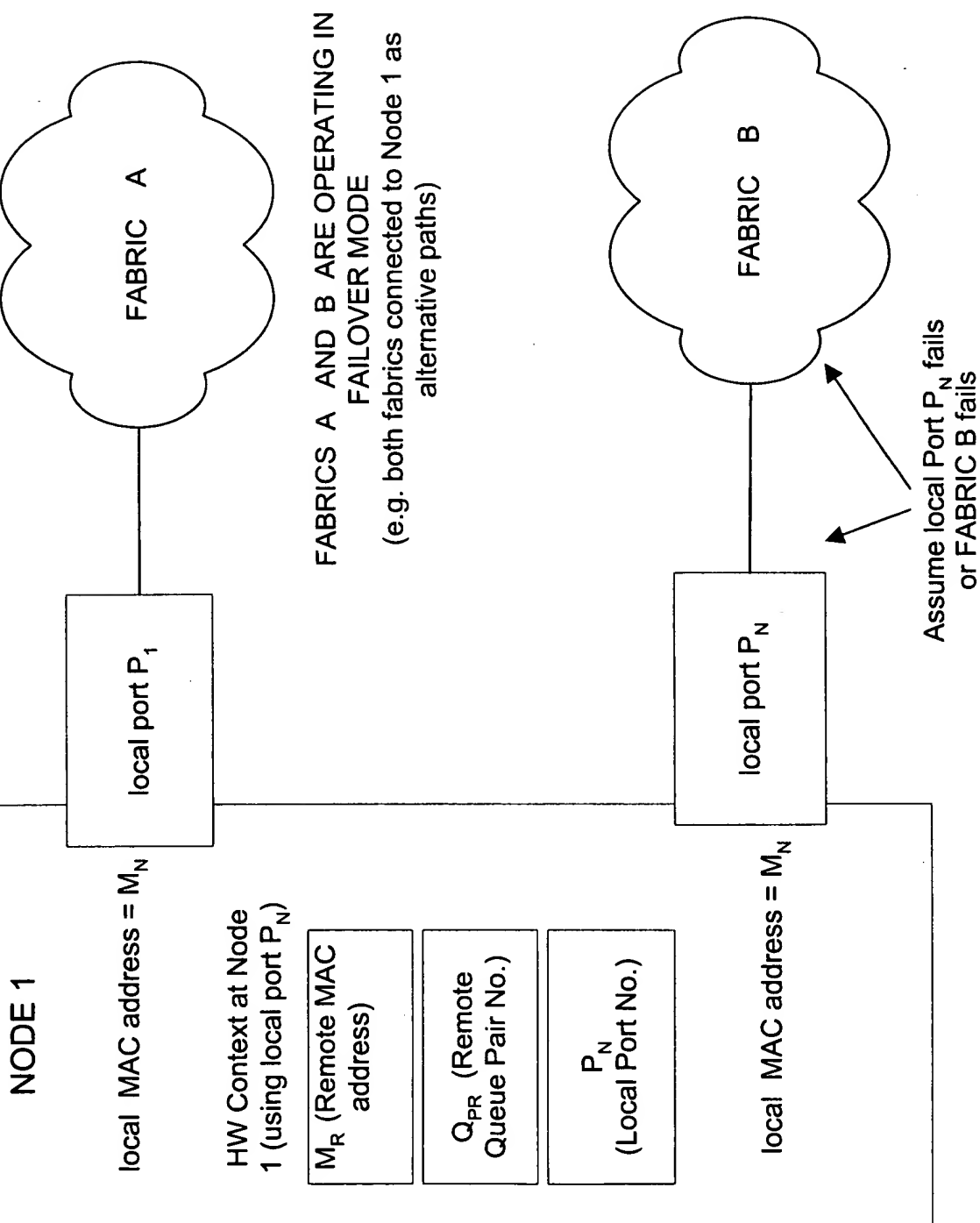


Fig. 6

002021 0250260

VIRTUAL-TO-PHYSICAL PORT MAP	
VIRTUAL PORT	PHYSICAL PORT
VP1	PP6
VP2	PP1
VP3	PP7
VP4	PP4
VP5	PP2
VP6	PP3
VPN	PPN

700

FIG. 7

00000000000000000000

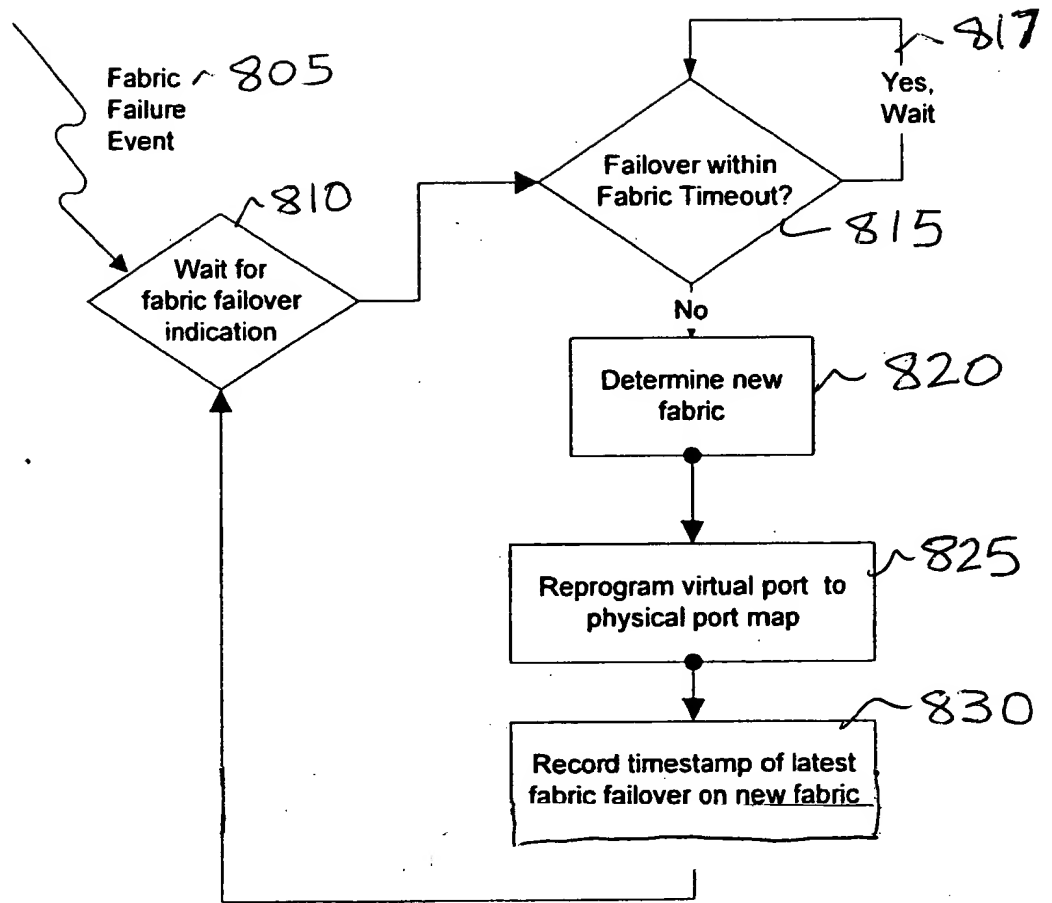


Fig. 8

One register
set per Channel

Virtual to physical
Port Map 700

One Entry per
Virtual Port #

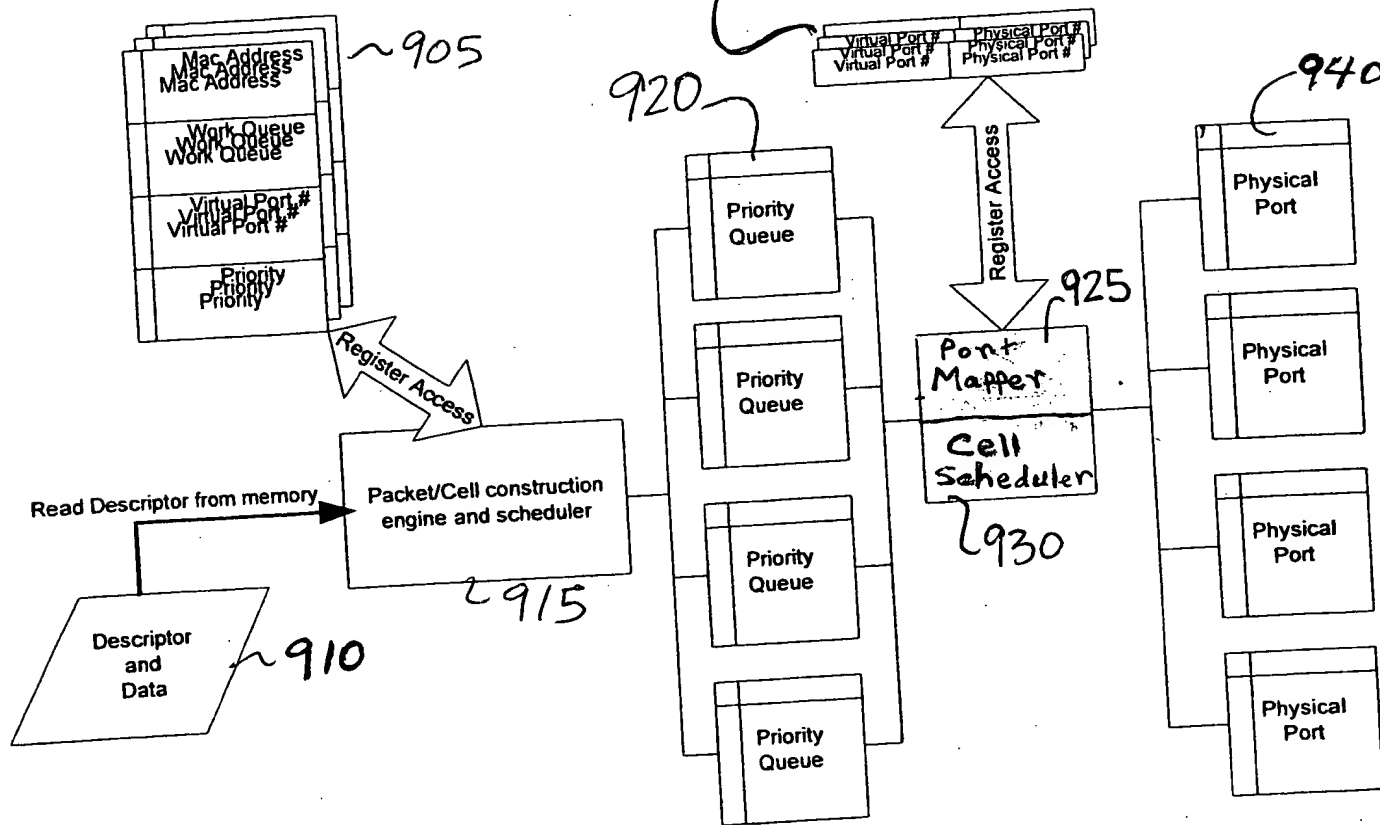


Fig. 9